



Contents

im

TRAN/PWR

6

Peripherals

7

Sensors

8

Elevator Connectors

Revisions

Rev	Description	Date	Approved
A	Prototype Release	23-Jul-10	J.H.
B	Pilot Release	21-Sep-10	DK

Microcontroller Solutions Group
6501 William Cannon Drive West
Austin, TX 78735-6598

This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.

Designer:
Jay Hartvigsen

Drawing Title:
TWR-K40X256

Drawn by:
Jay Hartvigsen

Page Title:
Table of Contents/Revisions

Approved:
Marilyn Hubbard

Size
C

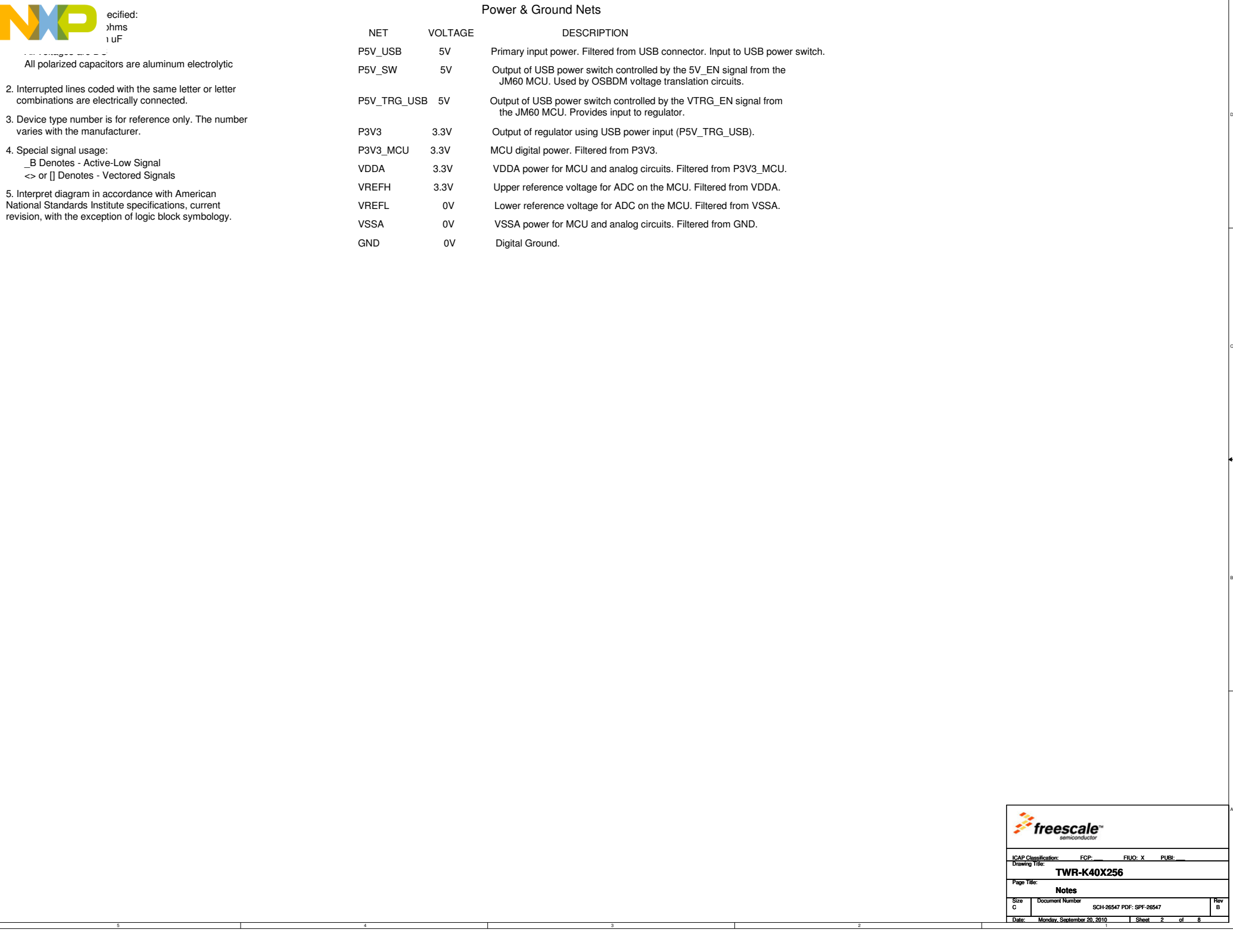
Document Number
SCH-26547 PDF: SPF-26547

Rev
B

Date:
Tuesday, September 21, 2010

Sheet
1

of
8



- 

ecified:
hms
1 uF
- All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

Power & Ground Nets		
NET	VOLTAGE	DESCRIPTION
P5V_USB	5V	Primary input power. Filtered from USB connector. Input to USB power switch.
P5V_SW	5V	Output of USB power switch controlled by the 5V_EN signal from the JM60 MCU. Used by OSBDM voltage translation circuits.
P5V_TRG_USB	5V	Output of USB power switch controlled by the VTRG_EN signal from the JM60 MCU. Provides input to regulator.
P3V3	3.3V	Output of regulator using USB power input (P5V_TRG_USB).
P3V3_MCU	3.3V	MCU digital power. Filtered from P3V3.
VDDA	3.3V	VDDA power for MCU and analog circuits. Filtered from P3V3_MCU.
VREFH	3.3V	Upper reference voltage for ADC on the MCU. Filtered from VDDA.
VREFL	0V	Lower reference voltage for ADC on the MCU. Filtered from VSSA.
VSSA	0V	VSSA power for MCU and analog circuits. Filtered from GND.
GND	0V	Digital Ground.

